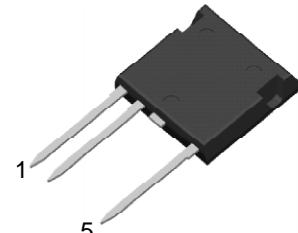
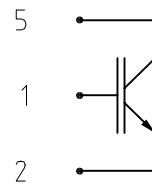


**High Voltage  
BIMOSFET™  
in High Voltage  
ISOPLUS i4-PAC™**

**IXBF 9N140 G  
IXBF 9N160 G**

<b>I<sub>C25</sub></b>	<b>= 7 A</b>
<b>V<sub>CES</sub></b>	<b>= 1400/1600 V</b>
<b>V<sub>CE(sat)</sub></b>	<b>= 4.9V</b>
<b>t<sub>f</sub></b>	<b>= 70 ns</b>

**Monolithic Bipolar MOS Transistor**



**IGBT**

Symbol	Conditions	Maximum Ratings		
V <sub>CES</sub>	T <sub>VJ</sub> = 25°C to 150°C	IXBF 9N140	1400	V
		IXBF 9N160	1600	V
V <sub>GES</sub>			± 20	V
I <sub>C25</sub>	T <sub>C</sub> = 25°C		7	A
I <sub>C90</sub>	T <sub>C</sub> = 90°C		4	A
I <sub>CM</sub>	{ V <sub>GE</sub> = 10/0 V; R <sub>G</sub> = 27 Ω; T <sub>VJ</sub> = 125°C V <sub>CEK</sub> }	RBSOA, Clamped inductive load; L = 100 μH	12	A
V <sub>CEK</sub>			0.8V <sub>CES</sub>	
P <sub>tot</sub>	T <sub>C</sub> = 25°C		70	W

Symbol	Conditions	Characteristic Values		
		(T <sub>VJ</sub> = 25°C, unless otherwise specified)		
		min.	typ.	max.
V <sub>CE(sat)</sub>	I <sub>C</sub> = 5 A; V <sub>GE</sub> = 15 V; T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 125°C	4.9 5.6	7 V	V
V <sub>GE(th)</sub>	I <sub>C</sub> = 0.5 mA; V <sub>GE</sub> = V <sub>CE</sub>	3.5	5.5	V
I <sub>CES</sub>	V <sub>CE</sub> = 0.8V <sub>CES</sub> ; V <sub>GE</sub> = 0 V; T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 125°C	0.1	0.1 mA mA	
I <sub>GES</sub>	V <sub>CE</sub> = 0 V; V <sub>GE</sub> = ± 20 V		500	nA
t <sub>d(on)</sub>	Inductive load, T <sub>VJ</sub> = 125°C V <sub>CE</sub> = 960 V; I <sub>C</sub> = 5 A V <sub>GE</sub> = 10/0 V; R <sub>G</sub> = 27 Ω	140		ns
t <sub>r</sub>		200		ns
t <sub>d(off)</sub>		120		ns
t <sub>f</sub>		70		ns
C <sub>ies</sub>	V <sub>CE</sub> = 25 V; V <sub>GE</sub> = 0 V; f = 1 MHz	550		pF
Q <sub>Gon</sub>	V <sub>CE</sub> = 600 V; V <sub>GE</sub> = 10 V; I <sub>C</sub> = 5 A	34		nC
V <sub>F</sub>	(reverse conduction); I <sub>F</sub> = 5 A	3.6		V
R <sub>thJC</sub>			1.75	K/W

**Features**

- High Voltage BIMOSFET™
  - substitute for high voltage MOSFETs with significantly lower voltage drop
  - MOSFET compatible control 10V turn on gate voltage
  - fast switching for high frequency operation
  - reverse conduction capability
- ISOPLUS i4-PAC™
  - high voltage package
  - isolated back surface
  - enlarged creepage towards heatsink
  - enlarged creepage between high voltage pins
  - application friendly pinout
  - high reliability
  - industry standard outline

**Applications**

- switched mode power supplies
- DC-DC converters
- resonant converters
- lamp ballasts
- laser generators, x ray generators

IXYS reserves the right to change limits, test conditions and dimensions.

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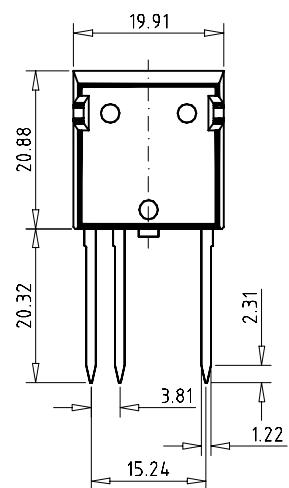
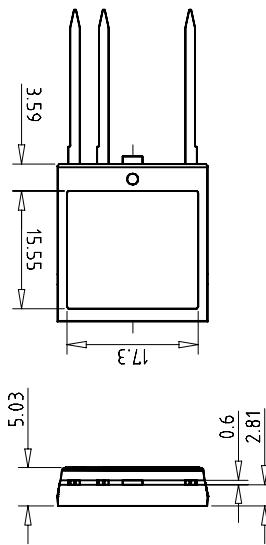
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**Component**

Symbol	Conditions	Maximum Ratings		
$T_{VJ}$		-55...+150	°C	
$T_{stg}$		-55...+125	°C	
$V_{ISOL}$	$I_{ISOL} \leq 1 \text{ mA}; 50/60 \text{ Hz}$	2500	V-	
$F_c$	mounting force with clip	20...120	N	

Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
$d_s d_A$	pin 2 - pin 5	7		mm
$d_s d_A$	pin - backside metal	5.5		mm
$R_{thCH}$	with heatsink compound	0.15		K/W
<b>Weight</b>		9		g

**Dimensions in mm (1 mm = 0.0394")**

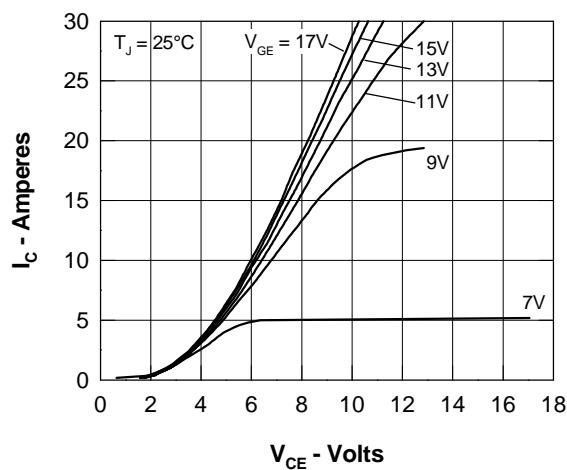


Fig. 1 Typ. Output Characteristics

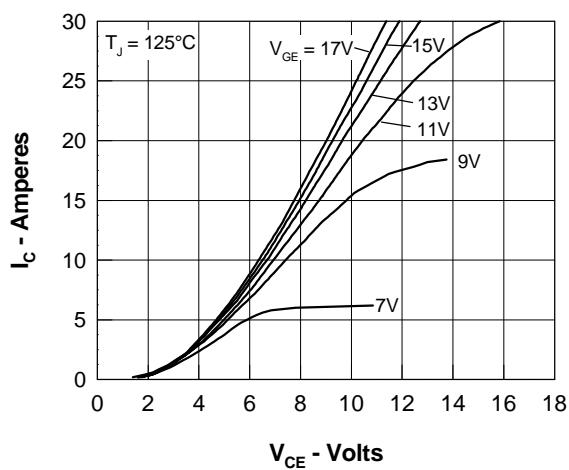


Fig. 2 Typ. Output Characteristics

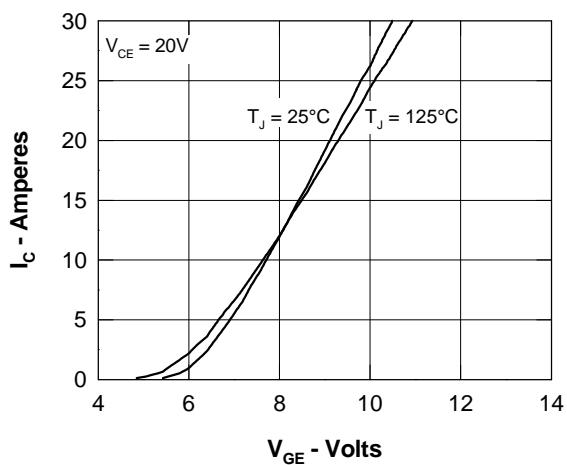


Fig. 3 Typ. Transfer Characteristics

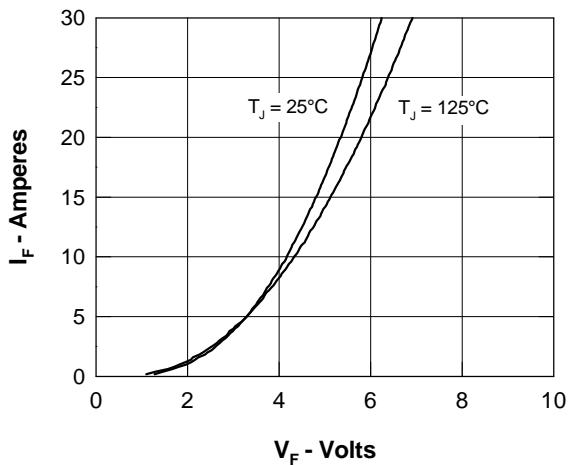


Fig. 4 Typ. Characteristics of Reverse Conduction

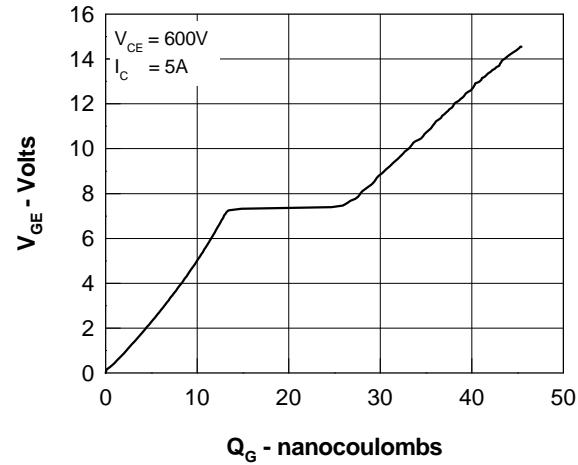


Fig. 5 Typ. Gate Charge characteristics

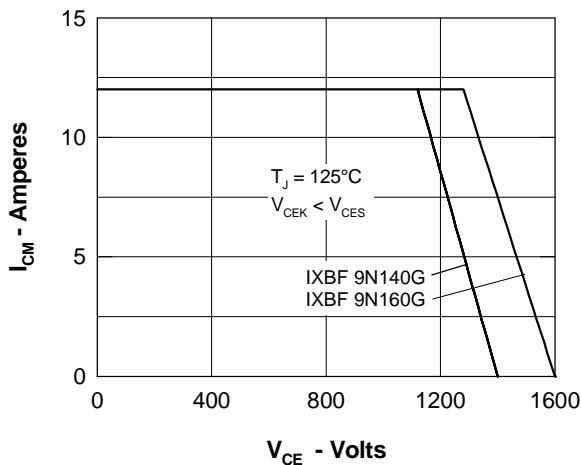


Fig. 6 Reverse Biased Safe Operating Area RBSOA

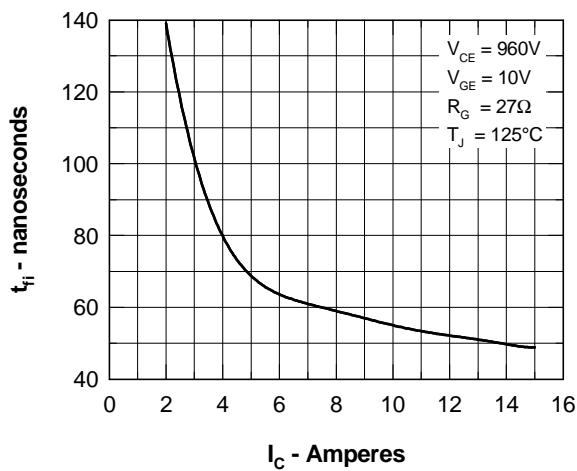


Fig. 7 Typ. Fall Time

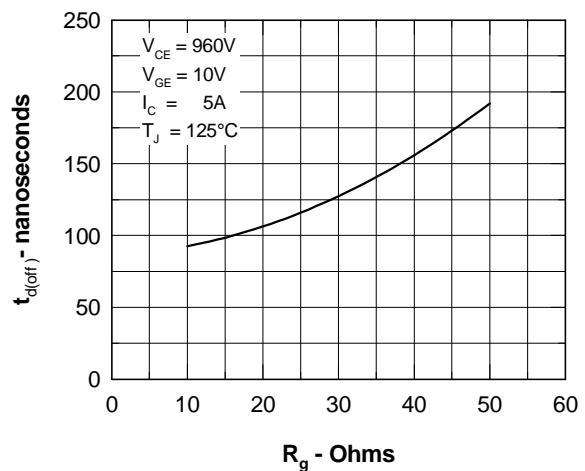


Fig. 8 Typ. Turn Off Delay Time

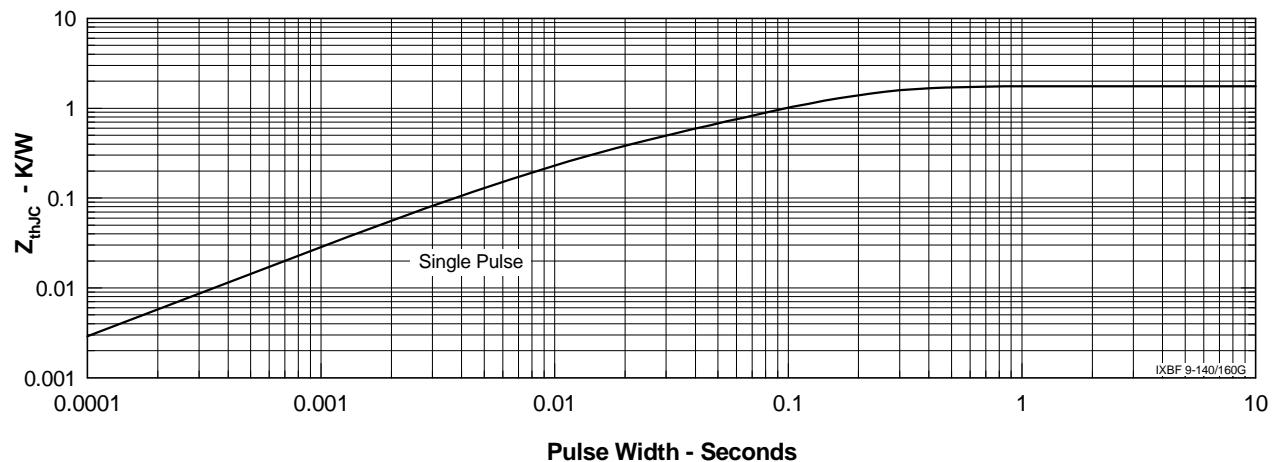


Fig. 9 Typ. Transient Thermal Impedance